

HARI PRASADH KALAIYARASI SUBRAMANIAN

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PROFILE SUMMARY

An Under Graduate Electrical Engineer, currently an Aptitude Trainer primarily focused on learning and teaching the basics of aptitude (Latest Pattern) for competitive exams (BANK SSC CAT GMAT). Looking forward to sharing my two years of teaching experience in your organization.

APTITUDE LEARNING AND TEACHING

- I Started preparing for competitive exams in January 2019.
- I started training students for competitive exams in January 2020.
- Cleared various Prelims Exams like RRB PO AND CLREK 2019, IBPS PO 2020, SBI CLERK 2021 and have a wide experience of exam patterns and new concepts from these exams in the past three years.
- Scored 90 percentile in Quantitative in GRE EXAM in 2018.

AREA OF INTEREST

- QUANTITATIVE APTITUDE
- LOGICAL REASONING
- CURRENT AFFAIRS
- CONTENT DEVELOPING

WORK EXPERIENCE

ORGANISATION	<u>DURATION</u>
Trichy Plus (BANK CAT GMAT)	<u>January 2020 - December 2020</u>
Veranda Race (Previously Chennai Race Coaching Institute) – BANK SSC	<u>January 2021 - December 2021</u>

ACADEMIC QUALIFICATION

Bachelor of Engineering (Electrical and Electronics Engineering), St Joseph's Institute of Technology, affiliated to Anna University, Chennai, May 2017.

TECHNICAL SKILLS

- C, C++ and basics of JAVA
- Programming in Microcontrollers using C and Embedded C
- MATLAB
- Microsoft Office

INTERNSHIP COURSE

Embedded Systems (PIC Controllers, ARM Processors-Cortex M0 – (M SERIES) , PCB design), ARM ACADEMY, CHENNAI, December 2017- January 2019.

Projects:

- Wheel Survivor Measurement, June-July 2018
 - Using Transformer 230/20V, Mosfets, Switches, PIC16F877A, Sensor, and wheel, developed a wheel survivor. My role is to design both the hardware and software of the product and test the developed product under various circumstances
 - Team size: 2
 - Software: microC, MPLAB IDE

- SPI Communication, August 2018
 - Using two PIC controllers of PIC16F877A develop the software to work either PIC's in master mode and another as a slave mode
 - Team size: 2
 - Software: microC, MPLAB IDE
- ARM Cortex M0 Processor, August 2018- December 2018
 - Using ARM cortex M0 learning board, experienced various basic aspects of ARM processor in C programming and its driver tools
 - Software: KEIL-ARM Processor

ACADEMIC PROJECTS

St. Joseph's Institute of Technology, Chennai

Adaptive Neuro-Fuzzy based for Power Quality Improvement, March 2016-May 2017

Team size: 2

Objective: To develop stable power flow via maintaining constant voltage and frequency range, irrespective of any changes occurring in the power system

Individual role:

- Involved in design of Unified Power Quality Conditioner (UPQC) for real and reactive power compensation
- Designed an algorithm for neuro-fuzzy logic control system
- Simulated the experiment using MATLAB (2015b)
- Used Adaptive Neuro-Fuzzy system to train and determine the required voltage for compensation

Wireless Power Transmission Network (WiTricity), August 2015-January 2016

Objective: To transmit power without cable wires similar to the normal Wi-Fi concept using the principle of electromagnetic induction and design model of a battery for storage purpose

K-MAP Design using Software, August 2014-December 2015

Team size: 2

Objective: To design the K-map (mathematical method) using the C-language software

Individual role: Designed the algorithm

PAPER PRESENTATIONS

- 'Utilizing the Maximum Renewable Energy Resources using Solar Energy', Agni College of Technology, Chennai, January 2017
- 'WiTricity', Anna University, Chennai, January 2016

Place: TRICHY ,
Date : / /2022.

YOURS SINCERELY,
K S HARI PRASADH.