

## SHIVA AGGARWAL

( 342 RANK- UPSC CSE-2018)

**B.Tech, Electrical and Communication Engineering**  
Delhi Technological University (formerly DCE)

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Address:- 340, GH-8 Sahyog Apartments Paschim vihar-110063



### OBJECTIVES

I am looking for working in a professional and thoughtful supervision where I can apply my knowledge and skills. **Hence I am presenting myself as a faculty for MATHS, QUANTITATIVE APTITUDE and GS ( any section including CURRENT AFFAIRS).**

### BASIC ACADEMIC CREDENTIALS

Qualification	Board/University	Year	Percentage
B.Tech (Electrical and Communication Engineering)	Delhi Technological University ( Formerly DCE )	2016	First division Grade - A
Intermediate	CBSE Kendriya Vidyalaya , Rohini	2012	92%
High School	CBSE Kendriya Vidyalaya , Rohini	2010	93.5%

### PROFESSIONAL RECORDS ( 7 years teaching experience in various esteemed institution )

<b><u>UPSC Civil Service Examination 2018</u></b>	I have cracked this year's UPSC Civil Services Examination with All India Rank 342.  I will be joining as IRS officer with effect from december 2019.  My optional subject was GEOGRAPHY for UPSC examinations.
<b><u>Vedanta ias</u></b>	Taught economics for 3 months for civil services aspirants for general studies mains and prelims preparations in 2018.
<b><u>Inception Coaching institute</u></b>	Taught Quantitative Aptitude for Banking, SSC, Railways etc for 14 months at Dr. Mukherjee Nagar center during mid 2016 to 2017 end.
<b><u>Narayani Engineers Institute</u></b>	Taught Maths and Quantitative Aptitude for Banking, SSC, Railways, NDA, AFCAT for one and half year during 2014 to 2016.
<b><u>Ultimate Success Academy</u></b>	Taught Physics and Maths for 3 years from 2012 to 2015.

<p><b><u>In Engineering</u></b></p>	<p>Finalist in best student paper award in IEEE international conference at Germany</p> <p>Asian certificate for clean and safe energy in 2013</p> <p>Won 15th national award for excellence in energy management on 'solar still- a solar car'</p> <p>Special appreciation award for 'solar still - a solar car'</p> <p>Completed course in prime era project planner</p> <p>Course 'circuit and system insight, from usa university in college campus in due time of 7 hrs</p> <p>Organized IEEE event on math's abacus ' matricka'</p> <p>Intern at ACCOUNT AGE INFOTECH PVT. LTD</p>
<p><b><u>RESEARCH PAPERS PUBLISHED</u></b></p>	<p>Efficient design of a embedded microcontroller advanced encryption standard using ascii model for core preprocessor using 8051 instructions.</p> <p>Next generation multipurpose processor.</p> <p>Van Neumann and Harvard architecture description leading to significant detailed analysis on EEPROM, flash EPROM.</p> <p>Watchdog timer, timer 0, timer 1, timer 2 scaling and prescaling effects on inputs.</p> <p>Nyquist rate, bode plot, root locus description of third order unstable system leading to stability from sampling difference criteria.</p> <p>Transverse electromagnetic wave in waveguide and isolators couplers and circulators.</p> <p>Klystron, Magnetron, Gunn diode, IMPATT devices efficiency while bunched according to Reed theory.</p> <p>Solar Cell Efficiency Using Maximum Power Tracking At Buck Boost Dc To Dc Converters</p>
<p><b><u>EXPERIENTIAL LEARNING</u></b></p>	<p><b>(WINTER INTERNSHIP PROGRAM)</b></p> <p>Intern At: <b>ACCOUNT AGE INFOTECH PVT LTD</b></p> <p>Duration of Internship: <b>25 JUNE 2014 - 22 JULY 2014</b></p>
<p><b><u>PROJECTS REPORT</u></b></p>	<p>Solar powered dc-dc buck boost converters</p> <p>Op amp integrators , differentiators , logarithms circuits</p> <p>Colpitt's oscillators , Wien bridge oscillator</p> <p>Finite State Machines Algorithms</p> <p>Synchronous and Asynchronous circuits</p>
<p><b><u>IT PROFICIENCY</u></b></p>	<p><b>Microsoft Office Word, Microsoft Office Excel, Microsoft Office Power Point</b></p> <p><b>LANGUAGE: <u>C++ , C</u></b></p> <p><b>MICROPROCESSOR LANGUAGE: <u>MATLAB , PSPICE</u></b></p>

## **COURSES STUDIED DURING GRADUATION**

Electricity and magnetism

Statistical physics

Hydraulics and Fluid dynamics

Thermal engineering

## **MISCELLANEOUS ACHIEVEMENTS**

Won a bronze medal in relay in zonal in year 2007.

Two time medalist at the Delhi region chess championship

Various certificates and medals and school level and regional level for various debates , projects, themes etc

## **INTERPERSONAL SKILL**

Ability to rapidly build relationship and set up trust.

Confident and Determined

Ability to cope up with different situations.

Always trying to find solutions in different situations rather than sitting

Believe in getting work done in due date and time

## **PERSONAL DETAILS**

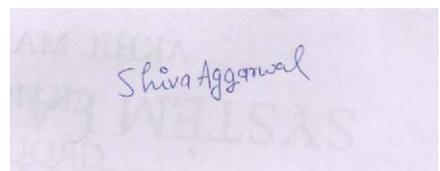
<b>Father's Name</b>	<b>:-</b> Mr. Anand Aggarwal
<b>Permanent Address</b>	<b>:-</b> 340, GH-8 Sahyog Apartments Paschim vihar-110063
<b>Date of Birth</b>	<b>:-</b> September 17, 1994
<b>Language Known</b>	<b>:-</b> English & Hindi
<b>Marital Status</b>	<b>:-</b> Unmarried
<b>Nationality/Religion</b>	<b>:-</b> Indian
<b>Interest &amp; Hobbies</b>	<b>:-</b> Chess, Cricket , Movies

## **DECLARATION**

I do hereby declare that the above information is true to the best of my knowledge.

Place: **Paschim Vihar**

**Shiva Aggarwal**



**(Signature)**